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Integrated RF front end
circuit design of
receivers and
synthesizers for
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communications, such
as LNA, mixers, PLL;
noise and linearity
analysis and
specifications; theory
and working
mechanism of
synthesizers and phase
noise analysis.

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concept of RF design
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[2] [1] T. S. Rappaport,
J. N. Murdock and F.

Gutierrez, "State of the
Art in 60-GHz

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in Proceedings of the
IEEE, vol. 99, no. 8, pp.
1390-1436, Aug. 2011.

D-band 110-170 GHz.

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The atmospheric absorption at 140 GHz is less than 1 dB/km [2]! Spectrum: Key to Wireless Capacity [1]

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digital circuits, and
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physical and
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Description: Analysis and design of electronic circuits for communication systems, with an emphasis on integrated circuits for wireless communication systems. Analysis of noise and distortion in amplifiers with application to radio receiver design. Power amplifier design with application to wireless radio transmitters.

Radio-frequency

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mixers, oscillators,
phase-locked loops,
modulators, and
demodulators.

EECS 142/242A

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focusses on CMOS RF
Integrated Circuits for
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Communications and
Phase-locked loops.

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The Nanoscale
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ultra-low power
integrated circuits for
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This paper provides a
brief overview of
present trends in the
development of
integrated circuit
technologies for
applications in the
wireless

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communications. Two broad categories of circuits are highlighted. The first is RF integrated circuits and the second is digital baseband processing circuits.

Integrated Circuit Technologies for Wireless Communications

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**State of the Art in
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(ADCs) and digital-to-analog converters (DACs), and even single-chip radios.

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photonic integration efforts to support advanced wireless communications in the E-band (60 – 90 GHz) and F-band (90- 140 GHz), having already demonstrated a wireless link using two

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